



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jiang et al.

Serial No.: 10/706,576

Filed: November 12, 2003

For: METHODS FOR ASSEMBLING
MULTIPLE SEMICONDUCTOR DEVICES

Confirmation No.: 6662

Examiner: J. Dolan

Group Art Unit: 2813

Attorney Docket No.: 2269-4886.1US
(01-0201.01/US)

Notice of Allowance Mailed:

September 29, 2006

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EL995985280US

Date of Deposit with USPS: December 29, 2006

Person making Deposit: Diane M. Sanders

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Sir:

Please amend the above-referenced application as follows:

Amendments to the specification begin on page 3 of this paper;

Amendments to the claims are set forth in the listing of the claims that begins on page 13 of this paper;

Corrections to the drawings are summarized on page 18 of this paper, with replacement sheets and annotated sheets showing the corrections enclosed herewith; and

Remarks start at page 19 of this paper.